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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : James R. Trethewey
Serial No. : 09/955,469
Filed : September 18, 2001
Assignee : Intel Corporation
Title : LOAD BALANCING AND FAULT TOLERANCE FOR SERVER-BASED
SOFTWARE APPLICATIONS

Art Unit : 2154
Examiner : John A. Follansbee

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

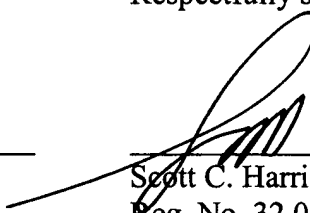
TRANSMITTAL OF FORMAL DRAWINGS

Please substitute the enclosed 4 sheets of formal drawings for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: November 9, 2004



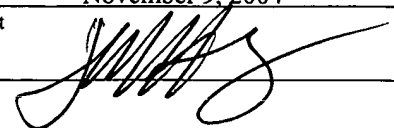
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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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